



FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.:
MUH-12869Applic. No.
10/723,907

Applicant

Marie Denison

Filing Date
November 26, 2003

Group Art Unit

2822



U.S. PATENT DOCUMENTS

| EXAMINER INITIALS | | PATENT NO. | DATE | PATENTEE | CLASS | SUB CLASS | FILING DATE |
|----------------------|---|------------|------|----------|-------|--------------|----------------|
| | A | | | | | | |
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FOREIGN PATENT DOCUMENT

| | | DOCUMENT NO. | DATE | COUNTRY | CLASS | SUB CLASS | TRANSL. YES NO |
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| | M | | | | | | |
| | N | | | | | | |

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

| | | |
|---|---|--|
|  | O | B. Jayant Baliga: "Modern Power Devices", Krieger Publishing Company, Malabar, Florida, 1987, pp. 81, 83 and 88 |
|  | P | Taylor Efland: "Lateral DMOS Structure Development for Advanced Power Technologies", TI Technical Journal, March/April 1994, pp. 10-24 |
| EXAMINER | | DATE CONSIDERED |
| | | 2-9-05 |

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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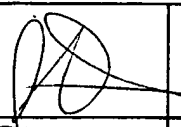
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|---|---|---|
|  | O | S. Merchant et al.: "High-Performance 13-65 V Rated LDMOS Transistors in an Advanced Smart Power Technology", <i>IEEE</i> , 1999, pp. 225-227 |
| | P | |

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